

JC971 U.S. PTO  
10/022683

Subclass	ISSUE CLASSIFICATION
Class	

PATENT NUMBER

## U.S. REISSUE Patent Application

10/022683	O.I.P.E. <i>K24</i> SCANNED	O.G. PUBLICATION DATE <i>04/09/02</i>	REISSUE PATENT DATE
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APPLICATION NO. 10/022683	CONT/PRIOR D	CLASS 257	SUBCLASS 632	ART UNIT 2811 2815	EXAMINER <i>S. Clark</i>
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APPLICANTS  
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 William Schaefer  
 Nikhil Kelkar

TITLE  
Semiconductor wafer having a bottom surface protective coatingPTO-2040  
12/99

SURRENDER OF ORIGINAL PATENT \_\_\_\_\_ (Ex. Initials)      ORIGINAL PATENT NUMBER \_\_\_\_\_

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Fig's Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____	(Primary Examiner)			ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	(Date)			Amount Due	Date Paid
FINAL SPRE REVIEW				ISSUE BATCH NUMBER	
(INITIALS) _____ (Legal Instruments Examiner) _____ (Date) _____					

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